

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

STANISLAV P. BAJUK ET AL.

Serial No. : 10/604,071

Patent No.: 7,080,344

Issued: **JULY 18, 2006**

For: **CODING OF FPGA AND
STANDARD CELL LOGIC IN A
TILING STRUCTURE**

ATTY. DOCKET NO.: BUR920030051US1

[illegible]

Examiner: **TUYEN P. TO**

Art Unit: 2825

Confirmation No.: 1070

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

Applicant respectfully submits the enclosed proposed Certificate of Correction, which corrects clerical errors made by the U.S. Patent and Trademark Office in the Letters Patent No. **US 7,080,344 B2**, issued July 18, 2006.

Respectfully submitted,

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO. : 7,080,344

APPLICATION NO.: 10/604,071

ISSUE DATE : July 18, 2006

INVENTOR(S) : Bajuk et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Claim 15, at column 7, line 10, delete "flexibleogic" and insert --flexible logic--.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

DILLON & YUDELL LLP
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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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